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Commissioner for Patents
Box Patent Applications
Washington D.C. 20231

BROADENING REISSUE PATENT APPLICATION TRANSMITTAL

INVENTOR(S): Nobuo FUDANUKI et al.

Original Patent Number: 6,054,872

Original Patent Issue Date (month/day/year): April 25, 2000

TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT WITH MIXED GATE ARRAY
AND STANDARD CELL

Total Pages: 26

In connection with this application, the following are enclosed:

APPLICATION ELEMENTS:

☒ Specification and Claims (amended, if appropriate)

☒ Drawing(s) (proposed amendments, if appropriate)

☒ Reissue Oath/Declaration (original or copy) (37 CFR 1.175) (PTO/SB/51 or 52))

Original U.S. Patent

☒ Surrender of Original Patent (37 CFR 1.178) (PTO/SB/53 or PTO/SB/54) OR

☐ Ribboned Original Patent Grant

☐ Affidavit/Declaration of Loss (PTO/SB/55)

Original U.S. Patent Current Assigned? ☒ YES ☐ NO
(if Yes, check applicable box(es))

☒ Written Consent of all Assignees

☒ 37 CFR 3.73(b) Statement

☐ Associate Power of Attorney and Change of Correspondence Address

ACCOMPANYING APPLICATION PARTS

☐ Foreign Priority Claim (35 USC 119) - (if applicable)

☒ Preliminary Amendment

☒ Return Receipt Postcard (MPEP 503)

Small Entity Statement(s)
Statement filed in prior application, status still proper and desired.
OTHER:

CORRESPONDENCE ADDRESS:

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FEE CALCULATIONS: including Amendments to claims. (Small entity fees indicated in parentheses.)

(1) For	(2) Number Filed	(3) Number Extra	(4) Rate	(5) Basic Fee \$690 (\$345)
Total Claims	65	45	x \$18 (x \$9)	810
Independent Claims	4	1	x \$78 (x \$39)	\$78.00
Surcharge Fee (37 C.F.R. §1.16(e))			\$130	\$0.00
			TOTAL FEE:	\$1,108.00


METHOD OF PAYMENT:

☒ A check in the amount of the above TOTAL FEE is attached. This amount is believed to be correct; however, the Commissioner is hereby authorized to charge any additional fee or credit any overpayment to Deposit Account No. 19-0741.

Payment by check is not enclosed. It is requested that the Patent and Trademark Office advise the undersigned of the period of time within which to file the TOTAL FEE.

Respectfully submitted,

Date: September 27, 2001


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No. 040301/0487

In re reissue patent application of

Nobuo FUDANUKI et al.

Serial No: Not Yet Assigned

Application for reissue of U.S. Patent No.
6,054,872, granted April 25, 2002

Filed: Herewith

For: SEMICONDUCTOR INTEGRATED CIRCUIT WITH MIXED GATE ARRAY
AND STANDARD CELL

CERTIFICATE UNDER 37 C.F.R. 3.73(b)

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

KABUSHIKI KAISHA TOSHIBA, a corporation, certifies that it is the assignee of the
entire right, title and interest in the patent application identified above by virtue of either:

A. ☒ An assignment from the inventor(s) of
the patent application identified above. The assignment was
recorded in the U.S. Patent and Trademark Office at Reel 9182,
Frame 0483, or for which a copy thereof is attached.

OR

B. ☐ A chain of title from the inventor, of the
patent application identified above, to the current assignee as
shown below:

1. From:

To:

☐ Additional documents in the chain of title are listed on a
supplemental sheet.

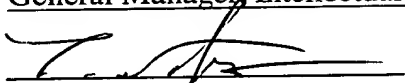
0963735-092701
"0260" SEZ9660

[] Copies of assignments or other documents in the chain of title are attached.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the assignee.

I hereby declare that all statements made herewith of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: September 7, 2001
Name: Takashi Nakayama
Title: General Manager, Intellectual Property Division
Signature: 

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